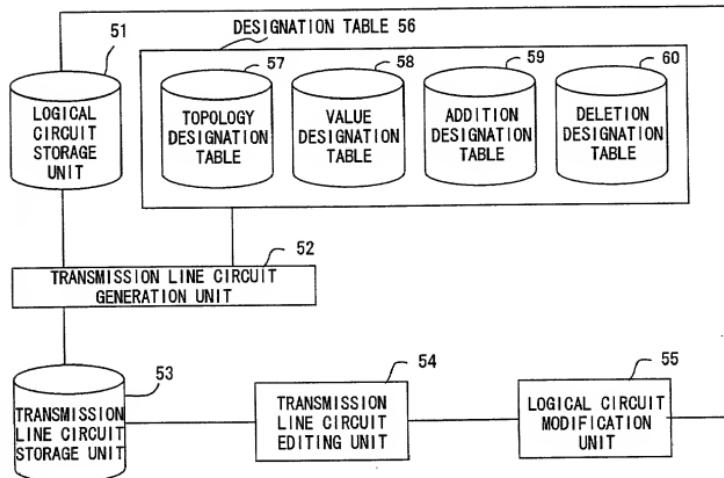


TO BE USED WITH

LOGICAL CIRCUIT DESIGN APPARATUS 50



F I G. 1

FIGURE 2: FIGURE 2

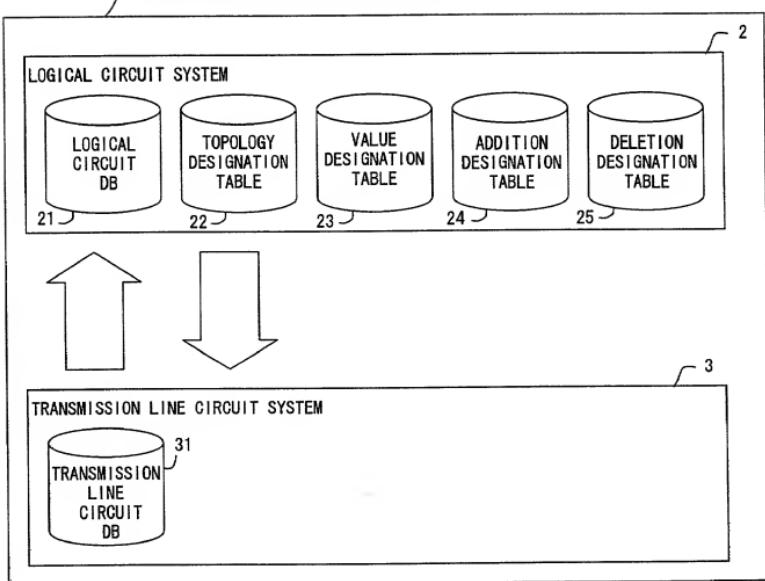
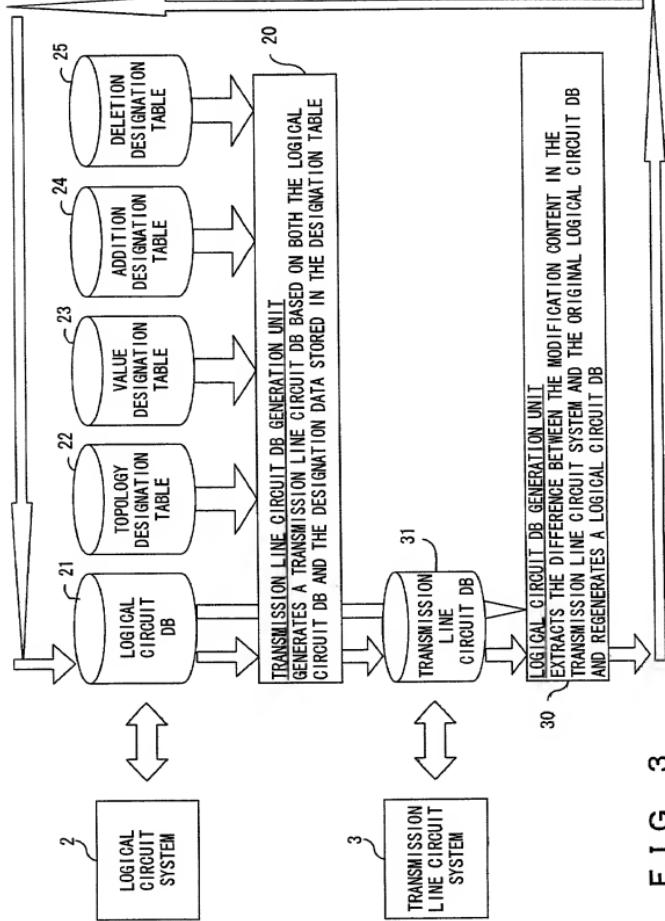
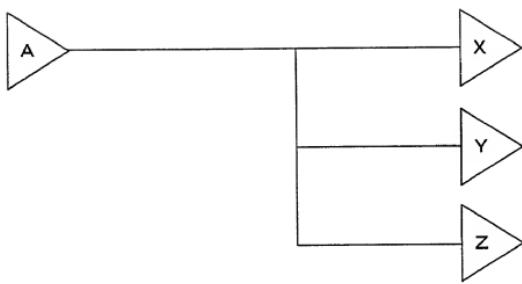


FIG. 2



F I G. 3



F I G. 4

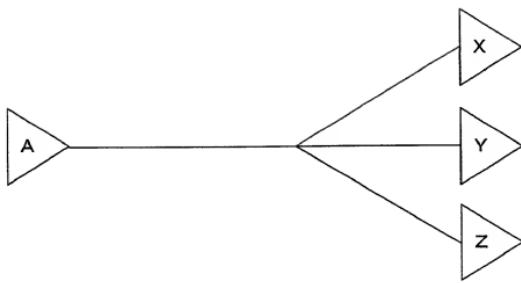
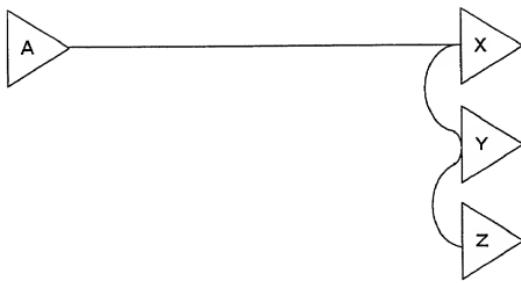
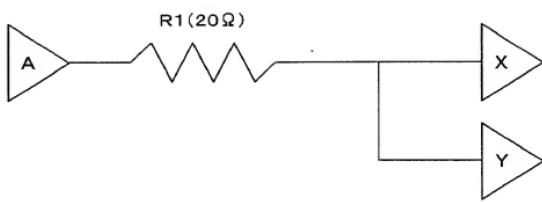


FIG. 5



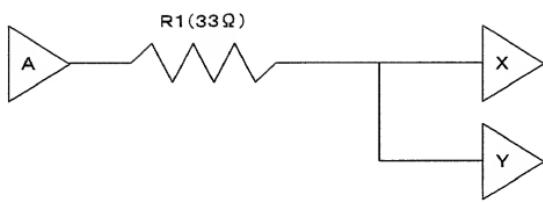
F I G. 6



F I G. 7

ITEM NO.	SYMBOL	COMPONENT DESCRIPTION	VALUE
1	R1	RESISTOR	33Ω

F I G. 8



F I G. 9

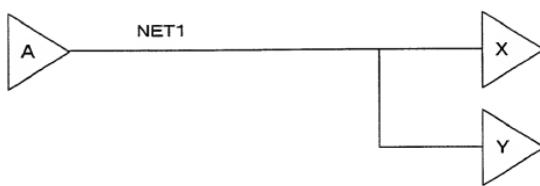
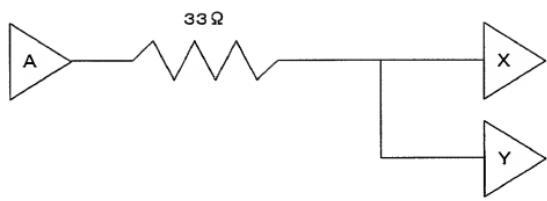


FIG. 10

FIGURE: YOUSSEFOU

ITEM NO.	NET	LOCATION	COMPONENT DESCRIPTION	VALUE
1	NET1	BY THE SIDE OF A	RESISTOR	33Ω

F I G. 11



F I G. 12

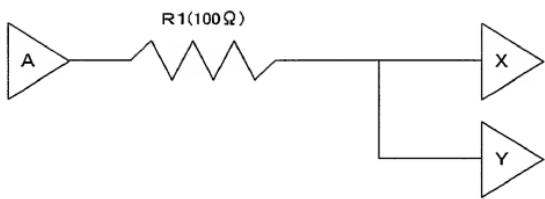


FIG. 13

ITEM NO.	SYMBOL
1	R1

F I G. 14

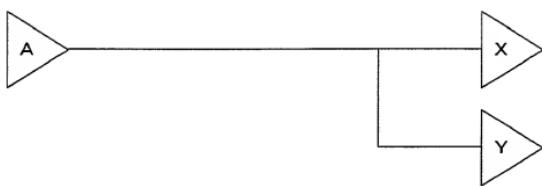
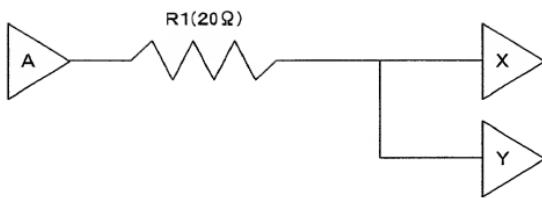
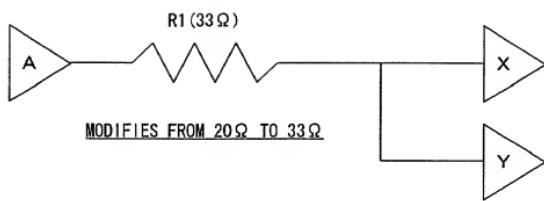


FIG. 15



F I G. 16



F I G. 17

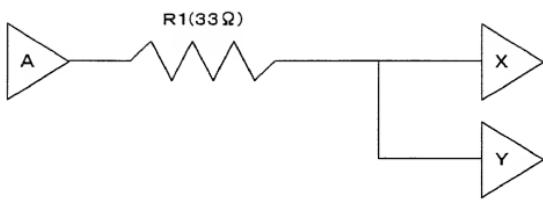
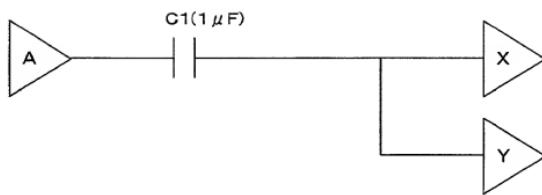
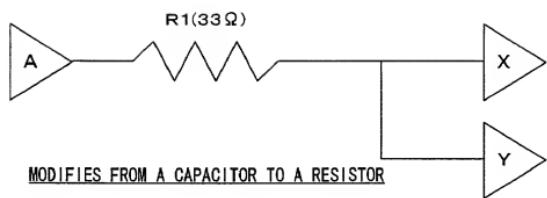


FIG. 18



F I G. 19



F I G. 20

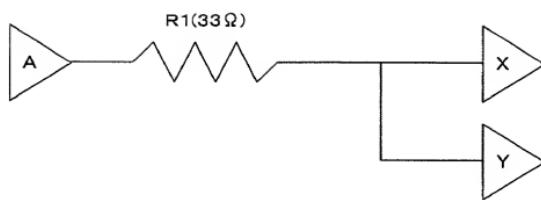


FIG. 21

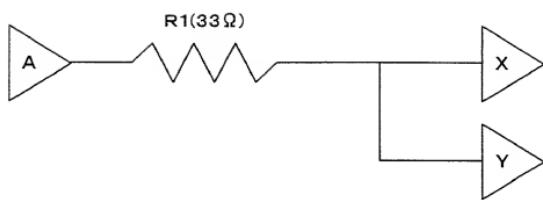
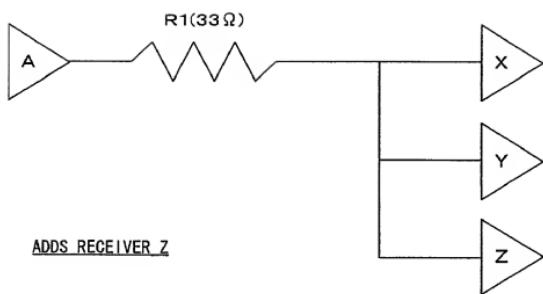
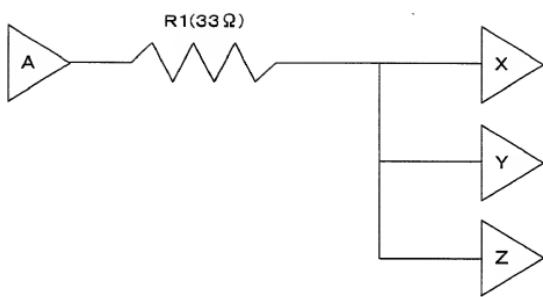


FIG. 22

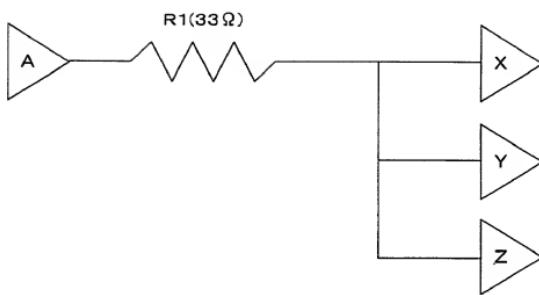


F I G. 23



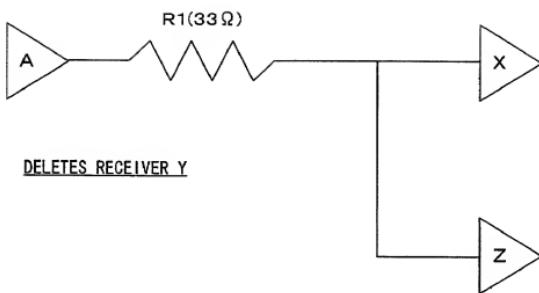
F I G. 2 4

06090502040204020402



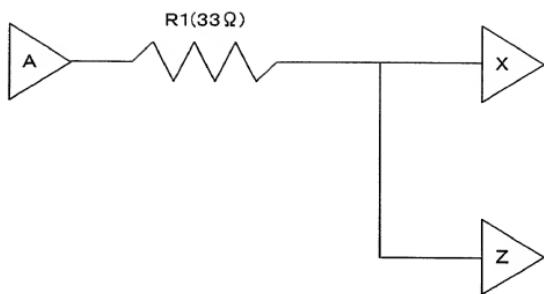
F I G. 25

00000001 : 00000000



DELETES RECEIVER Y

F I G. 26



F I G. 27

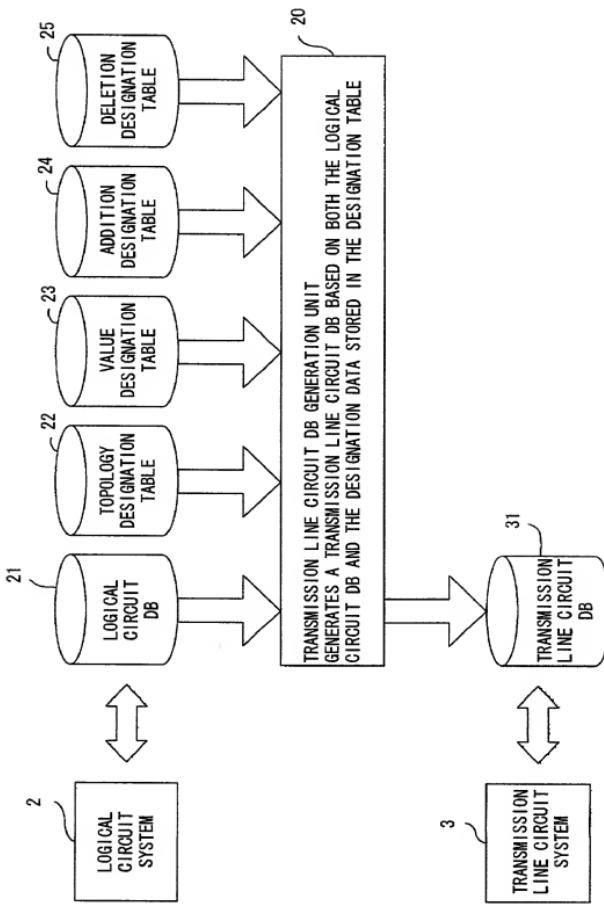


FIG. 28

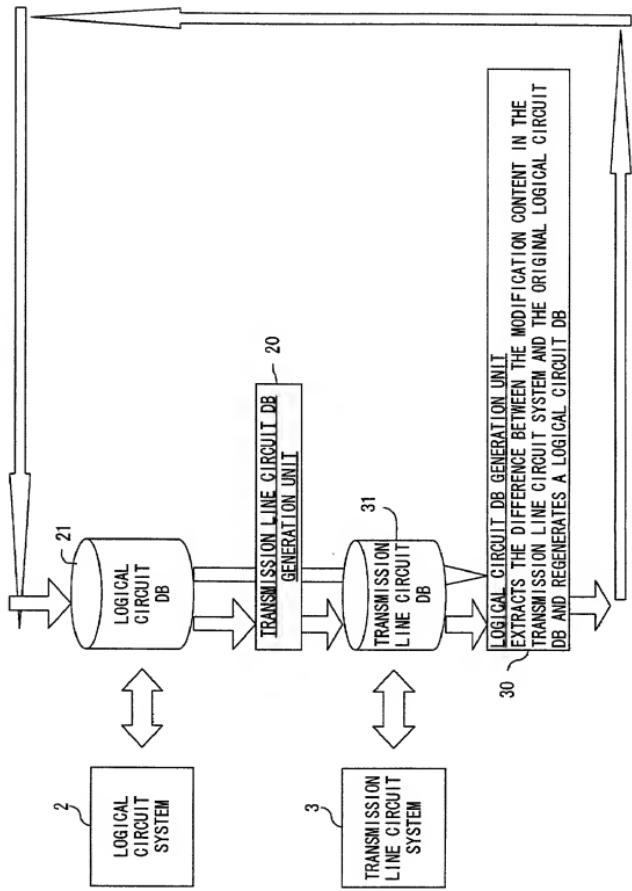
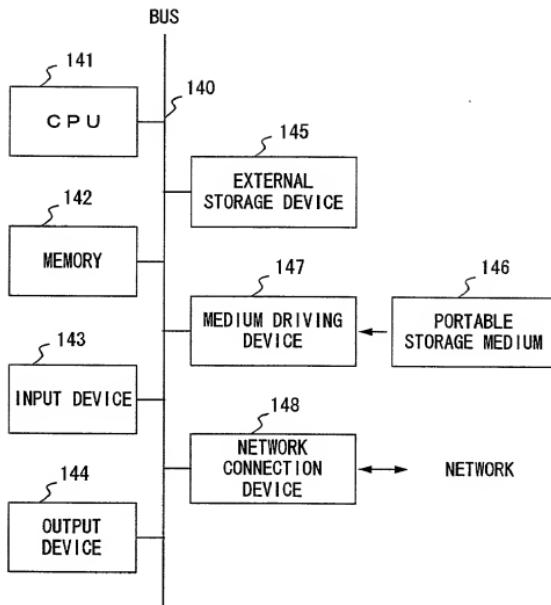
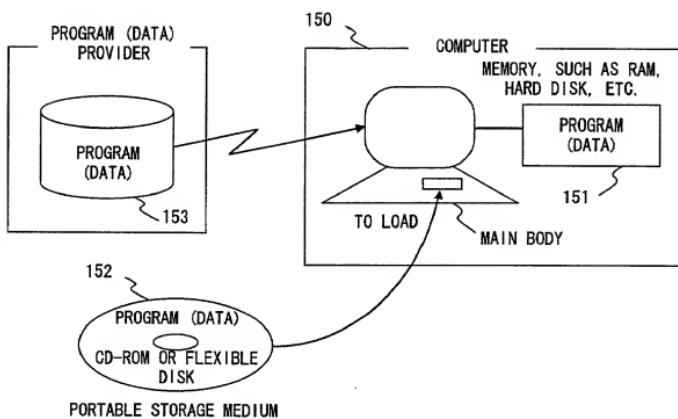


FIG. 29



F I G. 30

TO LOAD TO STORE



F I G. 31